



Atty. Dkt. No. 029471-0168

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroyuki TAKAHASHI et al.
Title: SEMICONDUCTOR MEMORY DEVICE
AND SEMICONDUCTOR MEMORY
DEVICE CONTROL METHOD
Appl. No.: 10/507,117
Filing Date: 9/10/2004
Examiner: Jung H. Hur
Art Unit: 2824
Confirmation 4725
Number:

AMENDMENT AND REPLY ACCOMPANYING RCE

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Final Office Action dated February 5, 2007, concerning the above-referenced patent application.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this document.

Remarks/Arguments begin on page 22 of this document.

Please amend the application as follows: